

**Patent**

**Docket No.: Intel 2207/17048**

**Assignee: Intel Corporation**

**Response Under 37 C.F.R. 1.116**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS	:	Stephan J. JOURDAN	<b>Expedited Procedure Examining Group</b>
<b>2181</b>			
SERIAL NO.	:	10/748,285	
FILED	:	December 29, 2003	
FOR	:	METHOD AND APPARATUS OF INDEXING AND UPDATING A TRACE CACHE BASED ON PAST BRANCH BEHAVIOR	
GROUP ART UNIT	:	2181	
EXAMINER	:	Tonia L. MEONSKE	

M/S: AF  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO FINAL OFFICE ACTION UNDER 37 C.F.R. 1.116**

Dear Sir:

The following remarks are respectfully submitted in response to the Final Office

Action dated July 30, 2007.

**A listing of claims** begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.